

# WEST Search History



DATE: Monday, November 07, 2011

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DB=PGPB,USPT; PLUR=NO; OP=ADJ

		<b>Hit Count</b>
<input type="checkbox"/>	L11 L10 and L6	194
<input type="checkbox"/>	L10 configuration with (data or information or instruction\$1) with interface	34110
<input type="checkbox"/>	L9 L8 and L6	618
<input type="checkbox"/>	L8 configur\$6 with interface	247137
<input type="checkbox"/>	L7 L6 and boundary with (condition\$1 or definition\$1)	33
<input type="checkbox"/>	L6 (326/38).ccls.	2728
<input type="checkbox"/>	L5 reconfigurable.ti. and fpga	668
<input type="checkbox"/>	L4 L3 same reconfigurable	78
<input type="checkbox"/>	L3 configur\$6 with boundar\$3	27282
<input type="checkbox"/>	L2 l1 same map\$4	2
<input type="checkbox"/>	L1 divided with circuit\$1 with virtual\$2 with connect\$4	44

END OF SEARCH HISTORY